

<b>LIA STATUS BYTE</b>	<u>bit</u>	<u>name</u>	<u>usage</u>
	0	INPUT/RESRV	Set when an Input or Amplifier overload is detected.
	1	FILTR	Set when a Time Constant filter overload is detected.
	2	OUTPT	Set when an Output overload is detected.
	3	UNLK	Set when a reference unlock is detected.
	4	RANGE	Set when the detection frequency switches ranges (harmonic x ref. frequency decreases below 199.21 Hz or increases above 203.12 Hz). Time constants above 30 s and Synchronous filtering are turned off in the upper frequency range.
	5	TC	Set when the time constant is changed indirectly, either by changing frequency range, dynamic reserve, filter slope or expand.
	6	TRIG	Set when data storage is triggered. Only if samples or scans are in externally triggered mode.
	7	unused	

The LIA Status bits stay set until cleared by reading or by the \*CLS command.

<b>ERROR STATUS BYTE</b>	<u>bit</u>	<u>name</u>	<u>usage</u>
	0	Unused	
	1	Backup Error	Set at power up when the battery backup has failed.
	2	RAM Error	Set when the RAM Memory test finds an error.
	3	Unused	
	4	ROM Error	Set when the ROM Memory test finds an error.
	5	GPIB Error	Set when GPIB fast data transfer mode aborted.
	6	DSP Error	Set when the DSP test finds an error.
	7	Math Error	Set when an internal math error occurs.

The Error Status bits stay set until cleared by reading or by the \*CLS command.