

In LC-3, both device status registers are extended to use bit 14 to request interrupts

In other words, write a 1 to bit 14 of DSR to turn on interrupts from the display (when the display becomes ready for a new character)

And write a 1 bit to bit 14 of KBSR to turn on keyboard interrupts (when the human types a key)

Stores to these registers do NOT overwrite bit 15.