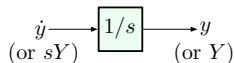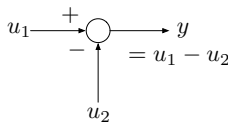# All-Integrator Diagrams

Our library will consist of three building blocks:

| integrator | summing junction | constant gain |

Two warnings:

- We can (and will) work either with u, y (time domain) or with U, Y (s-domain) — will often go back and forth
- When working with block diagrams, we typically ignore initial conditions.

This is the *lowest level* we will go to in lectures; in the labs, you will implement these blocks using op amps.